

# **GS1679** Cable Driver with HD/SD Capability

The GS1679 is a high-speed BiCMOS integrated circuit designed to drive one to four  $75\Omega$  coaxial cables. The GS1679 can drive data rates up to 1.485Gb/s, and provides two selectable slew rates in order to achieve compliance to SMPTE 292M, SMPTE 259M and SMPTE 344M.

The GS1679 accepts industry-standard differential input levels, including LVPECL and CML. The DISABLE1 and DISABLE2 pins power-down the first and second output drivers respectively, leaving the serial data output in a high-impedance state. The GS1679 features an adjustable output swing using an external bias resistor. The single-ended output swing is adjustable from 600mVpp to 1200mVpp. The GS1679 can be powered from either a 3.3V or a 2.5V supply. Power consumption is typically 110mW using a 2.5V power supply.

The GS1679 is forward footprint-compatible with Gennum's 3G GS2989, allowing for an easier migration of HD designs to 3G.

## **Key Features**

- SMPTE 292M, SMPTE 259M and SMPTE 344M compliant
- Supports data rates from 270Mb/s to 1.485Gb/s
- Supports DVB-ASI at 270Mb/s
- Dual differential coaxial-cable-driving outputs
  - selectable slew rates
  - adjustable output swing from 600mVpp to 1200mVpp
  - DISABLE control
- Wide common-mode range input buffer

- 100mV sensitivity
- Supports DC-coupling to industry-standard differential logic
- on-chip  $100\Omega$  differential data input termination
- Excellent output eye quality
- Power supply operation at 3.3V or 2.5V
- 110mW power consumption (2.5V supply)
- Operating temperature range: -40°C to +85°C
- Small footprint QFN package (4mm x 4mm)
- Pb-free and RoHS compliant
- Forward pin-compatible with Gennum's 3G GS2989

## **Typical Application Circuit**



### DOCUMENT IDENTIFICATION PRODUCT BRIEF

The product is in a development phase and specifications are subject to change without notice. Gennum reserves the right to remove the product at any time. Listing the product does not constitute an offer for sale.

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