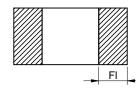
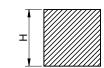
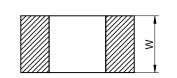
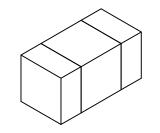
# Dimensions: [mm]









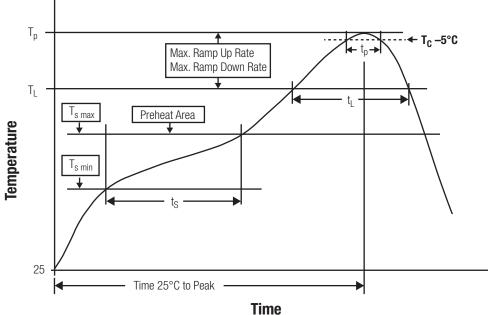
# **Mechanical Properties**

	•	
Properties	Definition	
Adhesive Strength of Termination	0402 & 0603	10 ±1 sec; 5 N
	> 0603	10 ±1 sec; 10 N
Vibration Resistance		all 3 irections, 2 hours each @ 10 - 55 Hz/ min., amplitu e 0.75 mm or 10 g
Resistance to Solder Heat	Specific	Refer to Sol ering Profile

_									5 mil	
	Würth Elektronik eiSos GmbH & Co. KG		CREATED	CREATED CHECKED GENERAL TO		GENERAL TOLERANCE		PROJECTION METHOD		
			KaS	PSL		DIN ISO 2768-1m		METHOD	-5-10-	
		DESCRIPTION				TECHNICAL REFERE	INCE	•		
		WCAP-CSGP Ceramic Capacitors				NP006034R7C100DFCT10000				
		0					ORDER CODE			
						88501	20060	91		
	_		SIZE	REVISION	STATUS		DATE (YYYY-MM-DD	))	BUSINESS UNIT	PAGE
		WÜRTH ELEKTRONIK	0603	001.000	Vali		2016-09-19		eiCap	2/7

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# **Classification Reflow Profile for SMT components:**



## **Classification Reflow Soldering Profile:**

Profile Feature		Value
Preheat Temperature Min	T <sub>s min</sub>	150 °C
Preheat Temperature Max	T <sub>s max</sub>	200 °C
Preheat Time $t_s$ from $T_{s min}$ to $T_{s max}$	t <sub>s</sub>	60 - 120 secon s
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )		3 °C/ secon max.
Liquidous Temperature	TL	217 °C
Time $t_L$ maintained above $T_L$	tL	60 - 150 secon s
Peak package body temperature	Т <sub>р</sub>	see table
Time within 5°C of actual peak temperaure	t <sub>p</sub>	20 - 30 secon s
Ramp-down Rate (T <sub>L</sub> to T <sub>P</sub> )		6 °C/ secon max.
Time 25°C to peak temperature		8 minutes max.

refer to IPC/ JEDEC J-STD-020E

## Package Classification Reflow Temperature:

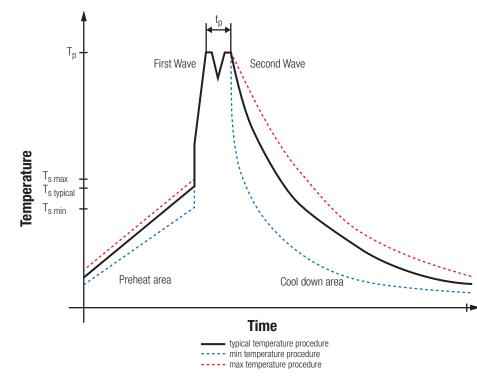
Properties	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
PB-Free Assembly   Package Thickness < 1.6 mm	260 °C	260 °C	260 °C
PB-Free Assembly   Package Thickness 1.6 mm - 2.5 mm	260 °C	250 °C	245 °C
PB-Free Assembly   Package Thickness $\ge$ 2.5 mm	250 °C	245 °C	245 °C

refer to IPC/ JEDEC J-STD-020E

Würth Elektronik elSos GmbH & Co. KG EMC & In uctive Solutions	CREATED KaS	CHECKED PSL		GENERAL TOLERANCE DIN ISO 2768-1m		PROJECTION METHOD		)-	
Max-Eyth-Str. 1 74638 Wal enburg Germany Tel. +49 (0) 79 42 945 - 0					TECHNICAL REFERENCE NP006034R7C100DFCT10000				
www.we-online.com eiSos@we-online.com					ORDER CODE	20060	91		
		REVISION	status Vali		DATE (YYYY-MM-DD 2016-09-19	)	BUSINESS UNIT eiCap		PAGE 3/7

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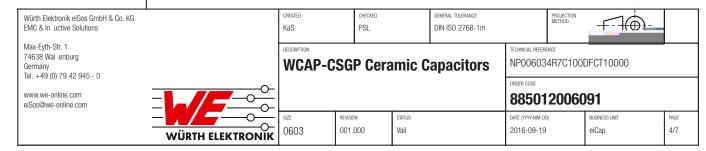
## **Classification Wave Soldering Profile:**



## **Classification Wave Soldering Profile:**

Profile Feature		Pb-Free Assembly	Sn-Pb Assembly
Preheat Temperature Min	T <sub>s min</sub>	100 °C	100 °C
Preheat Temperature Typical	T <sub>s typical</sub>	120 °C	120 °C
Preheat Temperature Max	T <sub>s max</sub>	130 °C	130 °C
Preheat Time $\rm t_s$ from $\rm T_{smin}$ to $\rm T_{smax}$	t <sub>s</sub>	70 secon s	70 secon s
Ramp-up Rate	ΔT	150 °C max.	150 °C max.
Peak temperature	Т <sub>р</sub>	250 °C - 260 °C	235 °C - 260 °C
Time of actual peak temperature	tp	max. 10 secon s max. 5 secon s each wave	max. 10 secon s max. 5 secon s each wave
Ramp-down Rate, Min		~ 2 K/ secon	~ 2 K/ secon
Ramp-down Rate, Typical		~ 3.5 K/ secon	~ 3.5 K/ secon
Ramp-down Rate, Max		~ 5 K/ secon	~ 5 K/ secon
Time 25°C to 25°C		4 minutes	4 minutes

refer to EN61760-1:2006



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# **Cautions and Warnings:**

# The following conditions apply to all goods within the product series of WCAP-CSGP of Würth Elektronik eiSos GmbH & Co. KG:

#### 1. General

- The capacitor is engineere, esigne an manufacture to be use within the atasheet specifie values.
- Do not use the capacitor neither short term nor long term outsi e the specifie values, which are given in the ata sheet.
- Do not apply any kin of flexural or compressive force onto sol ere or unsol ere component.
- Prevent the capacitor surface from any amage or scratches with sharp e ges (e.g. chassis, screw rivers, pincers)

## 2. Product specific

The responsibility for the applicability of customer specific pro ucts an use in a particular customer esign is always within the authority of the customer. All technical specifications for stan ar pro ucts o also apply to customer specific pro ucts.

Follow all instructions mentione in the ata sheet, especially the following items:

## 2.01 Storage conditions

- These ceramic capacitors must be store in stable con itions within an ambient temperature between 5°C to 40°C with a relative humi ity of
- The environment in which the capacitors are operate an store has to have atmospheric characteristics an must be free of ew con ensation an toxic gases (e.g. chlorine, ammonia, sulfur, hy rogen sulphi e an hy rogen sulfate).
- All pro ucts shall be use before the en of the perio of 12 months base on the pro uct ate co e, if not, a 100 sol erability cannot be guarantee .
- The capacitance tolerance as specifie within the atasheet is only vali on the ate of elivery.

## 2.02 Operating climatic conditions

- Do not excee the lower nor the upper specifie temperature un er no con ition.
- Be aware that the specifie capacitance tolerance is only vali at the ate elivery an accor ing specifie measurement criteria.
- Do not use the capacitors un er high humi ity, high temperature nor un er high or low atmospheric pressure which may affect capacitors reliability.
- Surface temperature inclu ing self-heating must be kept below the maximum operating temperature.
- The temperature rise of the capacitor's temperature compare to ambient temperature shall be below 20°C.
- Avoi any water or heavy ust on capacitors surface, which may cause electrical leakage, amage, overheating or corrosion.

## 2.03 Operating load conditions

- Do not use the capacitor with any higher than specifie voltage.
- Violation of the technical pro uct specifications such as excee ing the specifie voltage will voi the warranty.
- Operating voltage across the terminals inclu ing AC an DC peaks an AC or pulse overshooting, Vp-p as well as irregular voltage because of resonance or switching must be below the rate voltage.
- Due to self-heating the reliability of the capacitor may be re uce , if high fre uency AC or pulse is applie .
- Avoi any overloa or con itions that are not specifie in the capacitors atasheet.
- Consi er carefully possible specific changes of electrical characteristics like capacitance over temperature, voltage an time as well as the specific performance over fre uency for the actual use con itions. For etaile information see atasheet.

## 2.04 Design of the P.C. board

- The chip capacitor shall be locate to minimize any possible mechanical stress from eflection or boar wrap.
- It is recommene to position the chip capacitor in parallel to slits an perforations an as far away from slits, perforations, separation points, screw holes, frames an e ges of the P.C. boar to avoi mechanical stress.
- Determine the shape an size of the sol er pa s to have proper amount of sol er on the terminations as the amount of sol er at the terminations has a irect effect on the reliability of the capacitor.
- Provi e in ivi ual sol er pa s for each termination. Sol er pa s are specifie in the atasheet.
- The PCB esign (e.g. lan pattern esign an groun ing planes) must be evaluate for each in ivi ual circuit to achieve the optimal sol ering results.

#### 2.05 Mounting

- · Avoi any stress from the mounting hea to avoi cracks.
- A ust the bottom ea center of the mounting hea not to press on the P.C. boar surface.
- The mounting hea pressure has to be a uste to 1 N up to 3 N of static force.
- Provi e support from the bottom si e of the P.C. boar by a support pin for minimizing the impact energy from the mounting hea .
- Provi e sufficient close up imension, preventive maintenance an replacement of the centering aw to avoi a crack when it is worn
   out.

## 2.06 Adhesive

#### Selection of a hesive

- The a hesive shoul have sufficient coating an viscosity an shoul har en rapi ly.
- The a hesive shoul be strong enough to hol parts on the boar uring the mounting an sol er process an shoul have sufficient strength at high temperatures.
- The a hesive shoul have corrosion resistance, excellent insulation characteristics an no emission of toxic gasses nor any effect on the human bo y.

Würth Elektronik eiSos GmbH & Co. KG EMC & In uctive Solutions		CREATED KaS	CHECKED		GENERAL TOLERANCE DIN ISO 2768-1m		PROJECTION METHOD		
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www.we-online.com eiSos@we-online.com					ORDER CODE 885012006091				
			REVISION 001.000	status Vali		DATE (YYYY-MM-DD 2016-09-19		BUSINESS UNIT eiCap	PAGE 5/7

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Do not use too much a hesive to avoi pollution of the sol ering pa s.

#### 2.07 Soldering

- The etaile sol ering instructions for reflow an wave sol ering are given within Sol ering Specification in the atasheet.
- The sol ering profile has to be compliant with the technical sol ering specification, otherwise this will voi the warranty.
- Avoi any other than specifie temperature an / or time con itions uring sol ering.
- Customer nee s to ensure that the applie sol er paste, the paste thickness an sol er con itions are applicable to guarantee a
  sufficient sol er result accor ing to the relevant criteria of IPC-A-610.
- Excessive amount of sol er may lea to higher tensile force an chip cracking. Insufficient amount of sol er may etach the capacitor ue to effective contacts.
- Do not use excessive nor insufficient flux.
- Provi e enough washing when water-soluble flux is use
- Consi er the preheating con itions as follows to avoi thermal shock:
- 1. Wave sol ering 0603, 0805 Temperature ifference  $\Delta T \le 150^{\circ}C$
- 2. Reflow sol ering 1206 or less Temperature ifference  $\Delta T \le 190^{\circ}C$
- 3. Reflow sol ering 1210 or more Temperature ifference  $\Delta T \le 130^{\circ}C$
- 4. Manual sol ering 1206 or less Temperature ifference  $\Delta T \le 190^{\circ}C$
- 5. Manual sol ering 1210 or more Temperature ifference  $\Delta T \le 130^{\circ}C$
- It is recommene to use air for natural cooling. When ipping the chips into a solvent for cleaning, the temperature ifference (ΔT) must be less than 100°C.
- For reflow sol ering two times limitation is recommen e .
- Wave sol ering is recommen e only for the following case sizes: 0603 an 0805, thickness
- Please ensure for manual sol ering (sol er iron) not to excee the general temperature capabilities as specifie above.
- The following con itions are recommen e for sol er repair by sol er iron:
- Size Temp. (°C) Preheating Temp. (°C) Temperature ifference ( $\Delta$ T) Atmosphere
- 1206 or less  $330 \pm 20^{\circ}$ C >150°C  $\Delta T \le 190^{\circ}$ C
- 1210 or more 280°C max >150°C  $\Delta T \le 130^{\circ}$ C Room air

Typical time of actual peak temperature:

Do not make irect contact with the ceramic ielectric.

#### 2.08 Cleaning

- Cleaning agents that are use to clean the customer application might amage or change the characteristics of the component, bo y, pins or termination.
- Avoi Halogen in the flux or any contaminate flux as well as excessively high ultrasonic cleaning.

#### 2.09 Coating, molding and potting of the P.C. board

- When coating an mol ing the P.C. boar verify the uality influence on the capacitor.
- Verify the curing temperature an assure that there is no harmful ecomposing or reaction gas emission uring curing.
- Do not excee the maximal temperature rise of 20°C.
- If the pro uct is potte in customer applications, the potting material might shrink uring an after har ening. The pro uct is expose to
  the pressure of the potting material with the effect that the bo y an termination is possibly amage an so the electrical as well as
  the mechanical characteristics are en angere to be affecte. After the potting material is cure, the bo y an termination of the
  pro uct have to be checke if any re uce electrical or mechanical functions or estructions have occurre.

#### 2.10 Handling after chip is mounted

- Direct mechanical impact to the pro uct shall be prevente .
- After sol ering please pay attention not to ben , twist or istort the P.C. boar in han ling an storage.
- · Avoi excessive pressure uring the functional check of the P.C. boar .
- Avoi ben ing stress while breaking the P.C. boar .

#### 2.11 Handling of loose chip capacitor

- Once roppe o not use the chip capacitor.
- After mounting avoi piling up P.C. boar s to avoi hitting the chip capacitor of another boar .

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www.we-online.com eiSos@we-online.com					ORDER CODE 885012006091				
	WÜRTH ELEKTRONIK	size 0603	REVISION 001.000	status Vali		DATE (YYYY-MM-DE 2016-09-19	,	BUSINESS UNIT eiCap	PAGE 6/7

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# **Important Notes**

# The following conditions apply to all goods within the product range of Würth Elektronik eiSos GmbH & Co. KG:

#### **1. General Customer Responsibility**

Some goo s within the pro uct range of Würth Elektronik eiSos GmbH & Co. KG contain statements regar ing general suitability for certain application areas. These statements about suitability are base on our knowle ge an experience of typical re uirements concerning the areas, serve as general gui ance an cannot be estimate as bin ing statements about the suitability for a customer application. The responsibility for the applicability an use in a particular customer esign is always solely within the authority of the customer. Due to this fact it is up to the customer to evaluate, where appropriate to investigate an eci e whether the evice with the specific pro uct characteristics escribe in the pro uct specification is vali an suitable for the respective customer application or not.

#### 2. Customer Responsibility related to Specific, in particular Safety-Relevant Applications

It has to be clearly pointe out that the possibility of a malfunction of electronic components or failure before the en of the usual lifetime cannot be completely eliminate in the current state of the art, even if the pro ucts are operate within the range of the specifications. In certain customer applications re uiring a very high level of safety an especially in customer applications in which the malfunction or failure of an electronic component coul en anger human life or health it must be ensure by most a vance technological ai of suitable esign of the customer application that no in ury or amage is cause to thir parties in the event of malfunction or failure of an electronic component. Therefore, customer is cautione to verify that ata sheets are current before placing or ers. The current ata sheets can be ownloa e at www.we-online.com.

#### 3. Best Care and Attention

Any pro uct-specific notes, cautions an warnings must be strictly observe . Any isregar will result in the loss of warranty.

#### 4. Customer Support for Product Specifications

Some pro ucts within the pro uct range may contain substances which are sub ect to restrictions in certain uris ictions in or er to serve specific technical re uirements. Necessary information is available on re uest. In this case the fiel sales engineer or the internal sales person in charge shoul be contacte who will be happy to support in this matter.

#### 5. Product R&D

Due to constant pro uct improvement pro uct specifications may change from time to time. As a stan ar reporting proce ure of the Pro uct Change Notification (PCN) accor ing to the JEDEC-Stan ar inform about minor an ma or changes. In case of further ueries regar ing the PCN, the fiel sales engineer or the internal sales person in charge shoul be contacte. The basic responsibility of the customer as per Section 1 an 2 remains unaffecte.

#### 6. Product Life Cycle

Due to technical progress an economical evaluation we also reserve the right to iscontinue pro uction an elivery of pro ucts. As a stan ar reporting proce ure of the Pro uct Termination Notification (PTN) accor ing to the JEDEC-Stan ar we will inform at an early stage about inevitable pro uct iscontinuance. Accor ing to this we cannot guarantee that all pro ucts within our pro uct range will always be available. Therefore it nee s to be verifie with the fiel sales engineer or the internal sales person in charge about the current pro uct availability expectancy before or when the pro uct for application esign-in isposal is consi ere. The approach name above oes not apply in the case of in ivi ual agreements eviating from the foregoing for customer-specific pro ucts.

#### 7. Property Rights

All the rights for contractual pro ucts pro uce by Würth Elektronik eiSos GmbH & Co. KG on the basis of i eas, evelopment contracts as well as mo els or templates that are sub ect to copyright, patent or commercial protection supplie to the customer will remain with Würth Elektronik eiSos GmbH & Co. KG oes not warrant or represent that any license, either expresse or implie , is grante un er any patent right, copyright, mask work right, or other intellectual property right relating to any combination, application, or process in which Würth Elektronik eiSos GmbH & Co. KG components or services are use .

#### 8. General Terms and Conditions

Unless otherwise agree in in ivi ual contracts, all or ers are sub ect to the current version of the "General Terms an Con itions of Würth Elektronik eiSos Group", last version available at www.we-online.com.

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Max-Eyth-Str. 1 74638 Wal enburg Germany Tel. +49 (0) 79 42 945 - 0	WCAP-C	WCAP-CSGP Ceramic Capacitors				TECHNICAL REFERENCE NP006034R7C100DFCT10000			
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